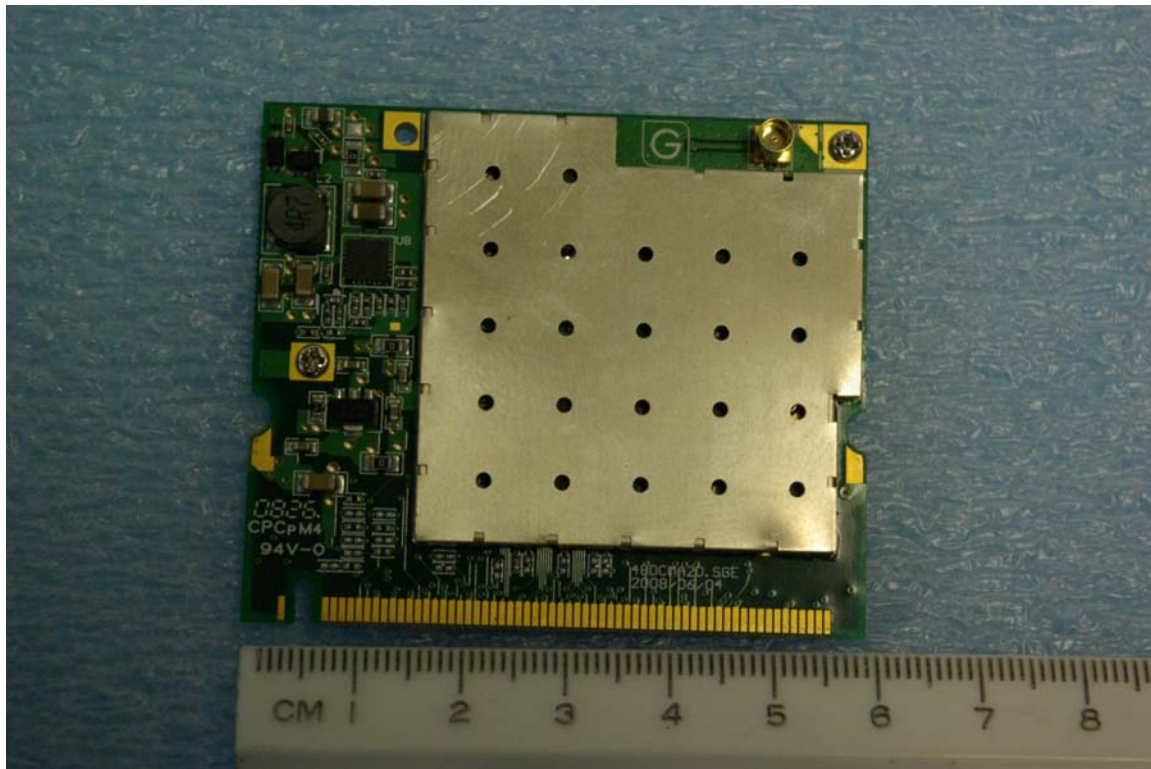


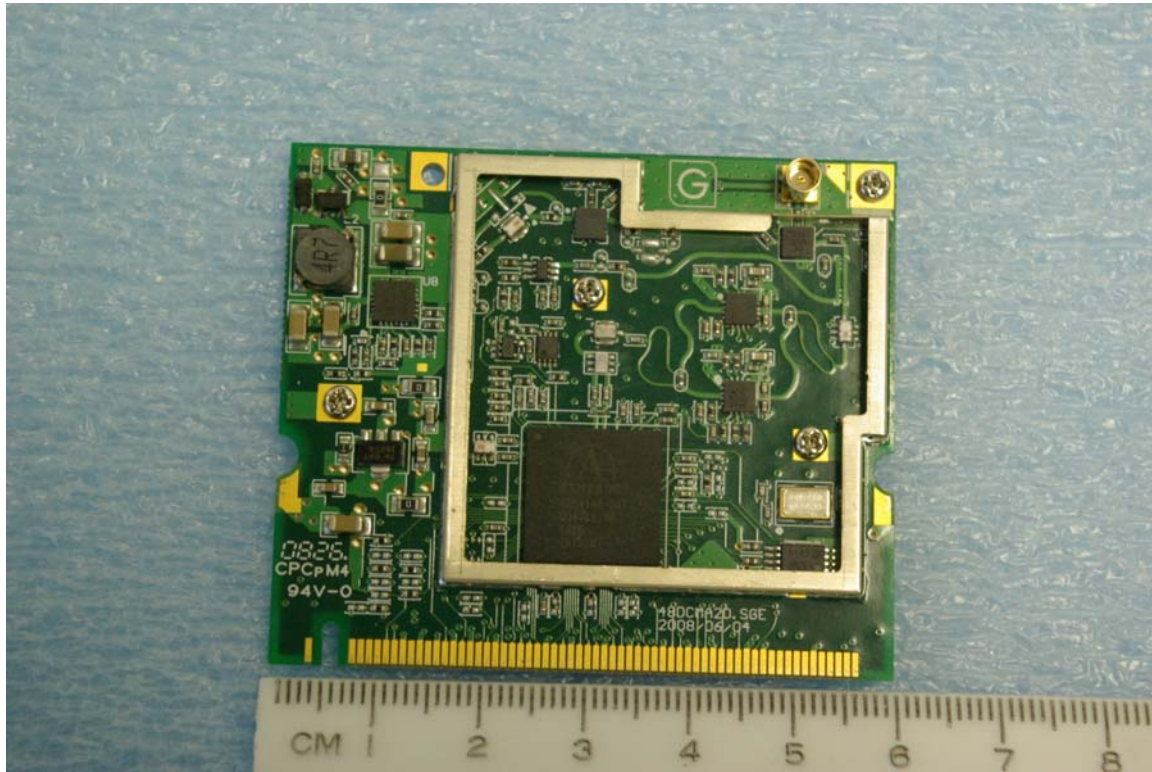
Internal photographs of equipment under test  
EUT on Support board



PCB with shield of EUT



PCB without shield



Back side of EUT PCB

